

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"20040155616"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:15
L2	0	"20040155616" and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (on) and off	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:15
L3	1	"20040155616" and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:34
L4	0	"20040155616" and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (on\$1 near4 state) and (off\$2 adj2 state)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:16
L5	11	318/443.ccls. and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:18
L6	11	318/443.ccls. and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (transistor switch\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:18
L7	2	318/443.ccls. and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:19
L8	4	318/443.ccls. and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 puls\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:21
L9	55	318/254 and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 puls\$4) and ((open\$2 adj2 circuit\$4) same (short\$2 adj2 circuit\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:21
L10	20	318/254.ccls. and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 puls\$4) and ((open\$2 adj2 circuit\$4) same (short\$2 adj2 circuit\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:26
L11	20	318/254.ccls. and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 puls\$4) and ((open\$2 adj2 circuit\$4) same (short\$2 adj2 circuit\$4)) and open and short	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:26
L12	20	318/254.ccls. and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 puls\$4) and ((open\$2 adj2 circuit\$4) same (short\$2 adj2 circuit\$4)) and open\$4 and short\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:29
L13	18	318/254.ccls. and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 puls\$4) and ((open\$2 adj2 circuit\$4) same (short\$2 adj2 circuit\$4)) and open\$4 and short\$4 and high\$4 and low\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:40

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L14	1	"20040155616" and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and low\$4 and high\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:34
L15	0	318/254.ccls. and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 puls\$4) and ((open\$2 adj2 circuit\$4) same (short\$2 adj2 circuit\$4)) and open\$4 and short\$4 and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and ((short\$2 adj2 circuit\$4) near4 low\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:41
L16	7	"318"/\$ and (open\$2 adj2 circuit\$4) and (short\$2 adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 puls\$4) and ((open\$2 adj2 circuit\$4) same (short\$2 adj2 circuit\$4)) and open\$4 and short\$4 and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and ((short\$2 adj2 circuit\$4) near4 low\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:54
L17	8	"318"/\$ and (open\$2 adj2 circuit\$4) and ((clos\$4 short\$2) adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 puls\$4) and ((open\$2 adj2 circuit\$4) same ((clos\$4 short\$2) adj2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and (((short\$2 clos\$4) adj2 circuit\$4) near4 low\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 15:59
L18	301	(open\$2 adj2 circuit\$4) and ((clos\$4 short\$2) adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 puls\$4) and ((open\$2 adj2 circuit\$4) same ((clos\$4 short\$2) adj2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and (((short\$2 clos\$4) adj2 circuit\$4) near4 low\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:01
L19	75	(open\$2 adj2 circuit\$4) and ((clos\$4 short\$2) adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 ) and ((open\$2 adj2 circuit\$4) same ((clos\$4 short\$2) adj2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and (((short\$2 clos\$4) adj2 circuit\$4) near4 low\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:03
L20	21	(open\$2 adj2 circuit\$4) and ((clos\$4 short\$2) adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 ) and ((open\$2 adj2 circuit\$4) same ((clos\$4 short\$2) adj2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and (((short\$2 clos\$4) adj2 circuit\$4) near4 low\$4) and (motor actuator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:04
L21	20	(open\$2 adj2 circuit\$4) and ((clos\$4 short\$2) adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 ) and ((open\$2 adj2 circuit\$4) same ((clos\$4 short\$2) adj2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and (((short\$2 clos\$4) adj2 circuit\$4) near4 low\$4) and (motor )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 17:46
L22	17	21 not 17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:23
L23	80	(open\$2 adj2 circuit\$4) and ((clos\$4 short\$2) adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 chop\$4) and ((open\$2 adj2 circuit\$4) same ((clos\$4 short\$2) adj2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and (((short\$2 clos\$4) adj2 circuit\$4) near4 low\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:25
L24	60	23 not (21 17)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:26
L25	75	(open\$2 adj2 circuit\$4) and ((clos\$4 short\$2) adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 ) and ((open\$2 adj2 circuit\$4) same ((clos\$4 short\$2) adj2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and (((short\$2 clos\$4) adj2 circuit\$4) near4 low\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 16:25

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L26	55	25 not (21 17)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 17:58
L27	9	(US-5793169-\$ or US-5783916-\$ or US-4972133-\$ or US-4633156-\$ or US-5384495-\$ or US-5161112-\$ or US-4789834-\$ or US-6921475-\$ or US-6049446-\$).did.	USPAT	OR	ON	2006/11/12 16:38
L28	7	27 and (pwm modulat\$4 chop\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 17:39
L29	11	"5161112"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 17:39
L30	22	(open\$2 near2 circuit\$4) and ((clos\$4 short\$2) near2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 ) and ((open\$2 near2 circuit\$4) same ((clos\$4 short\$2) near2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and high\$4 and low\$4 and ((open\$2 near2 circuit\$4) near4 high\$4) and (((short\$2 clos\$4) near2 circuit\$4) near4 low\$4) and (motor )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 17:44
L31	9	(open\$2 adj2 circuit\$4) and ((clos\$4 short\$2) adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 ) and ((open\$2 adj2 circuit\$4) same ((clos\$4 short\$2) adj2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and (((short\$2 clos\$4) adj2 circuit\$4) near4 low\$4) and (machine)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 17:47
L32	6	(open\$2 adj2 circuit\$4) and ((clos\$4 short\$2) adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 ) and ((open\$2 adj2 circuit\$4) same ((clos\$4 short\$2) adj2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and high\$4 and low\$4 and ((open\$2 adj2 circuit\$4) near4 high\$4) and (((short\$2 clos\$4) adj2 circuit\$4) near4 low\$4) and (actuator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 17:55
L33	76	(open\$2 adj2 circuit\$4) and ((clos\$4 short\$2) adj2 circuit\$4) and (transistor switch\$4) and (pwm modulat\$4 ) and ((open\$2 adj2 circuit\$4) same ((clos\$4 short\$2) adj2 circuit\$4)) and open\$4 and (clos\$4 short\$4) and (hi\$1 high\$4) and (lo\$1 low\$4 ) and ((open\$2 adj2 circuit\$4) near4 (hi\$1 high\$4)) and (((short\$2 clos\$4) adj2 circuit\$4) near4 (lo\$1 low\$4) )	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 17:58
L34	1	33 not (25 21 17)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 17:59
L35	2	"6469469" and (pwm modulat\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 18:00
L36	1	"6469469" and (pwm modulat\$4) and (short\$4) and (open\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/12 18:00